Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Previously Presented) A power supply device comprising:

a capacitor unit in which capacitors are interconnected in series or in series-parallel;

a charging unit for charging the capacitor unit at a constant current;

a detecting unit for detecting voltage on a high potential side of each capacitor;

a determining unit for determining existence of an abnormality by performing calculation based on the voltage detected by the detecting unit; and

a communication unit for outputting a determining result from the determining unit,

wherein

the determining unit is adapted to determine that a capacitor of the capacitor unit is abnormal based on at least two of the following conditions:

a) over-voltage abnormal condition when a difference between respective voltages on the high potential side of the capacitor and an adjacent series capacitor exceeds upper-limit voltage "Va",

b) under-voltage abnormal condition when the difference between respective voltages on the high potential side of the capacitor and the adjacent series capacitor is lower than lower-limit voltage "Vb", and

c) negative voltage abnormal condition when a voltage value on the high potential side of the capacitor is negative.

2. (Cancelled)

3. (Original) The power supply device according to claim 1,

wherein lower-limit voltage value "Vb" is expressed by

$Vb = Vc / (2N)$,

where "Vc" is a charge voltage value of the capacitor unit and "N" is series number of the capacitors.

4. (Previously Presented) The power supply device according to claim 1,

wherein the determination unit starts determining if the capacitor in the capacitor unit is abnormal after charging has begun, and before charge voltage value "Vc" of the capacitor unit exceeds a predetermined voltage value "Vd".

5. (Currently Amended) The power supply device according to ~~claim 1~~claim 4,

wherein the predetermined voltage value "Vd" is expressed by

$Vd = Vt \times \{ 1 + (N - 1 - M) \times (1 - dev) / (1 + dev) \} - \alpha$,

where "Vt" is a withstand voltage value per capacitor cell, "dev" is a capacity variation of the capacitors, "N" is series number of capacitors, "M" is the number of series stages including short-failed capacitors, and "α" is a detection error margin.

6. (Previously Presented) The power supply device according to claim 1,

wherein the upper-limit voltage is set as a withstand voltage value per one cell of the capacitors.